

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re PATENT APPLICATION of:

Puneet Gupta et al.

Appln. No.: Unknown

Art Unit:

Filed: herewith

Examiner: Unknown

For: INTEGRATED CIRCUIT LOGIC WITH
SELF COMPENSATING BLOCK
DELAYS

INFORMATION DISCLOSURE STATEMENT

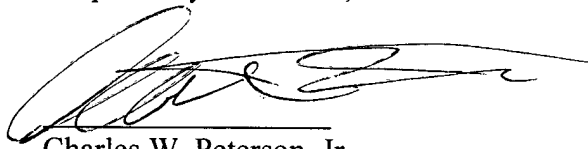
Mail Stop PATENT APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir,

Pursuant to the duty of disclosure under 37 C.F.R. §1.56, submitted herewith is an Information Disclosure Statement (IDS) including a Form PTO-1449 and a copy of one (1) non-patent publication. Pursuant to a Pre-OG Notice dated July 11, 2003 waiving requirement for copies of U.S. Patents and Published Applications, copies of Patents and Published Applications cited on the PTO-1449 are not included.

Since this IDS is being submitted with the application and prior to a first office action, no certification or fee is believed required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to all documents submitted. Consideration of the submitted documents is respectfully requested.

Respectfully Submitted,



Charles W. Peterson, Jr.
Registration No. 34,406

February 26, 2004

(Date)

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				Application Number	
				Filing Date	
				First Named Inventor	Gupta et al.
				Art Unit	
				Examiner Name	
Sheet	1	of	2	Attorney Docket Number	YOR920040091US1

[illegible][illegible]

Examiner Signature		Date Considered	
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449B/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete if Known

Application Number

Filing Date

First Named Inventor

Gupta et al.

Art Unit

Examiner Name

Sheet

2

of

2

Attorney Docket Number

YOR920040091US1

NON PATENT LITERATURE DOCUMENTS

[illegible]

Examiner Signature		Date Considered	
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